

2/6

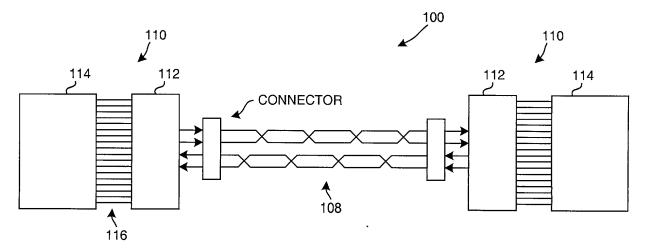


FIG. 1 (PRIOR ART)

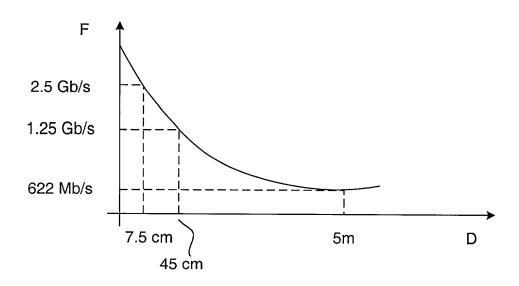


FIG. 3

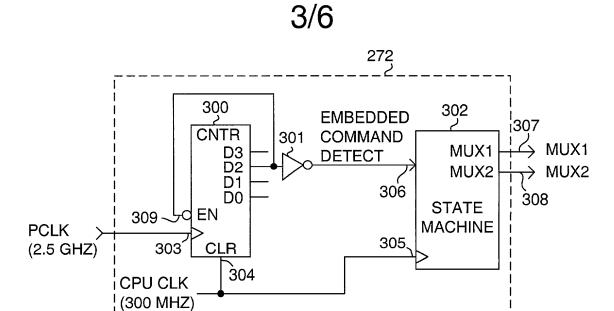


FIG. 4

EMBEDDED COMMAND DETECT 0000 0000 0100 0100 0100 0010 0001 0000 0001 0000 0000 COUNT **PCLK** (2.5 GHZ) **CPU CLK** (300 MHZ) ↑ T2 T1 FIG. 5 **EMBEDDED COMMAND DETECT** 0000 0001 0000 0000 0000 0000 0000 0000 COUNT **PCLK** (2.5 GHZ) **CPU CLK** (300 MHZ) ↑ T3 门 T1 FIG. 6

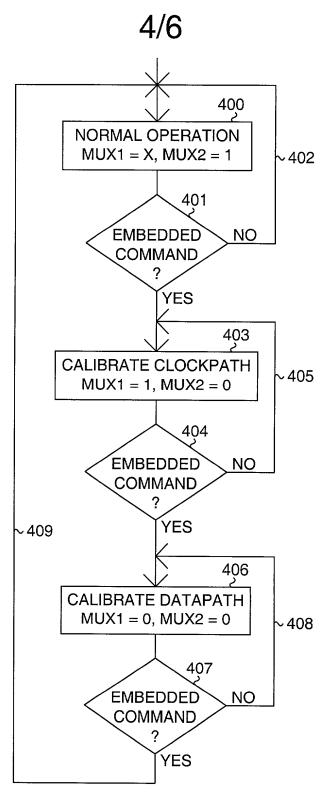


FIG. 7

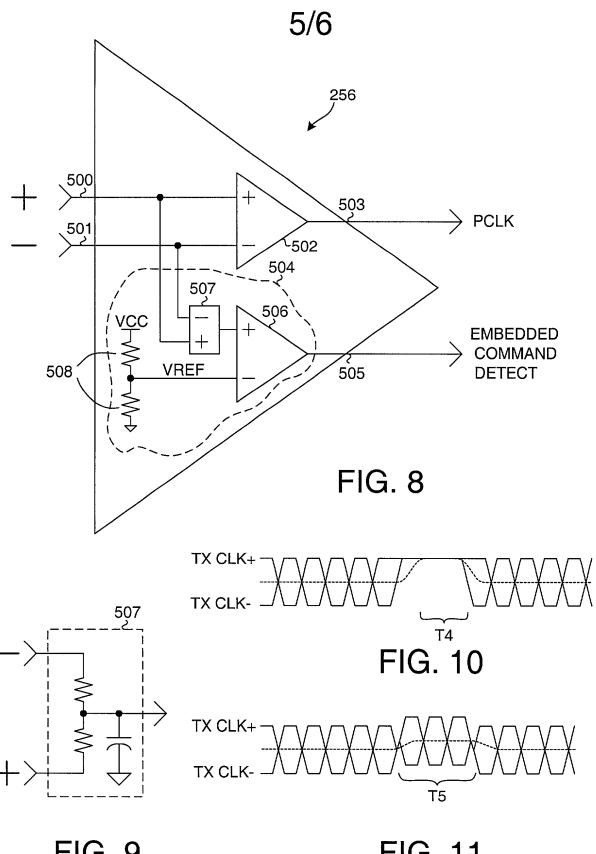


FIG. 9

FIG. 11

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